IN THE CLAIMS:

Please amend Claims 1, 2 and 23-25 as follows:

Please cancel Claim 26.

1. (Currently Amended) A display device comprising:

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2,..., Sn, Sn+1, Sn+2,..., S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L,..., GrL, ..., GmL (1 ≤r ≤m); and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R,..., GrR, ..., GmR (1 ≤r ≤m), wherein:

the pixels connected to the source signal lines S1, S2,..., Sn are supplied with the selection signals from the first gate signal lines G1L, G2L,..., GrR, ..., GmL;

the pixels connected to the source signal lines Sn+1, SN+2,..., S2n are supplied with the selection signals from the second gate signal lines G1R, G2R,..., GrR, ..., GmR;

the selection signal starts to be supplied to the second gate signal line ~~G1R~~ GrR while the selection signal is supplied to the first gate signal line ~~G1L~~ GrL; and

the selection signal starts to be supplied to the first gate signal line ~~G2L~~ Gr+1L while the selection signal is supplied to the second gate signal line ~~G1R~~ GrR,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate,

wherein the selection signal to the first gate signal line Gr+1L rises to a static state

after the selection signal to the first signal line GrL falls, and

wherein the selection signal to the first gate signal line Gr+1R rises to the static state after the selection signal to the first signal line GrR falls.

2. (Currently Amended) A display device comprising:

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2, ... , Sn, Sn+1, Sn+2, ... , S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L, ... , GrL, ..., GmL ($1 \leq r \leq m$) ; and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, ... , GrR, ..., GmR($1 \leq r \leq m$), wherein:

the pixels connected to the source signal lines S1, S2, ..., Sn are supplied with the selection signals from the first gate signal lines G1L, G2L, ... , GrL, ..., GmL;

the pixels connected to the source signal lines Sn+1, Sn+2, ... , S2n are supplied with the selection signals from the second gate signal lines G1R, G2R, ... , GrR, ...,GmR; and

the selection signals are sequentially supplied to the first gate signal line G1L, the second gate signal line G1R, the first gate signal line G2L, the second gate signal line G2R, ... , the first gate signal line GmL, and the second gate signal line GmR in this order with a delay of a half period between the respective adjacent gate signal lines,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate

wherein the selection signal to the first gate signal line Gr+1L rises to a static state after the selection signal to the first signal line GrL falls, and

wherein the selection signal to the first gate signal line Gr+1R rises to the static state after the selection signal to the first signal line GrR falls.

3. (Original) A rear projector comprising three display devices according to claim 1.

4. (Original) A rear projector comprising three display devices according to claim 2.

5. (Original) A front projector comprising three display devices according to claim 1.

6. (Original) A front projector comprising three display devices according to claim 2.

7. (Original) A rear projector comprising one display device according to claim 1.

8. (Original) A rear projector comprising one display device according to claim 2.

9. (Original) A front projector comprising one display device according to claim 1.

10. (Original) A front projector comprising one display device according to claim 2.

11. (Original) A head mount display comprising a display device according to claim 1.

12. (Original) A head mount display comprising a display device according to claim 2.

13. (Original) A computer comprising a display device according to claim 1.

14. (Original) A Computer comprising a display device according to claim 2.

15. (Original) A video camera comprising a display device according to claim 1.

16. (Original) A video camera comprising a display device according to claim 2.

17. (Original) A DVD player comprising a display device according to claim 1.

18. (Original) A DVD player comprising a display device according to claim 2.

19. (Original) A display device comprising a display device according to claim 1.

20. (Original) A display device comprising a display device according to claim 2.

21. (Original) A display device according to claim 1 is a liquid crystal display device.

22. (Original) A display device according to claim ~~2~~ 12 is a liquid crystal display device.

23. (Currently Amended) A method of driving an active matrix display device comprising:

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2, ... , Sn, Sn+1, Sn+2, ... , S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L, ... , GrL,..., GmL (1 ≤r ≤m); and

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a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, ... , GrR,....,GmR (1 ≤r ≤m),, wherein said method comprises the steps of:

supplying the pixels connected to the source signal lines S1, S2, ..., Sn with the selection signals from the first gate signal lines G1L, G2L, ..., GrL,... GmL;

supplying the pixels connected to the source signal lines Sn+1, Sn+2, ..., S2n with the selection signals from the second gate signal lines G1R, G2R, ..., GrR,...., GmR;

starting to supply the selection signal to the second gate signal line ~~G1R~~ GrR while the selection signal is supplied to the first gate signal line ~~G1L~~ GrL; and

starting to supply the selection signal to the first gate signal line ~~G1L~~ Gr+1L while the section signal is supplied to the second gate signal line ~~G1R~~ GrR,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate

wherein the selection signal to the first gate signal line Gr+1L rises to a static state after the selection signal to the first signal line GrL falls, and

wherein the selection signal to the first gate signal line Gr+1R rises to the static state after the selection signal to the first signal line GrR falls.

24. (Currently Amended) A method of driving an active matrix display device comprising:

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2,..., Sn, Sn+1, Sn+2,..., S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L, ..., GrL,...,GmL($1 \leq r \leq m$); and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, ..., GrR,.... GmR ($1 \leq r \leq m$), wherein said method comprises the steps of:

supplying the pixels connected to the source signal lines S1, S2,..., Sn with the selection signals from the first gate lines G1L, G2L, ..., GrL,...,GmL;

supplying the pixels connected to the source signal lines Sn+1, Sn+2, ..., S2n with the selection signals from the second gate lines G1R, G2R, ..., GrR,....GmR; and

sequentially supplying the selection signals to the first gate signal line G1L, the second gate signal line G1R, the first gate signal line G2L, the second gate signal line G2R, ... , the first gate signal line GmL, and the second gate signal line GmR in this order with a delay of a half period between the respective adjacent gate signal lines,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate

wherein the selection signal to the first gate signal line Gr+1L rises to a static state

after the selection signal to the first signal line GrL falls, and

wherein the selection signal to the first gate signal line Gr+1R rises to the static state after the selection signal to the first signal line GrR falls.

25. (Currently Amended) A display device comprising:

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2,..., Sn, Sn+1, Sn+2,..., S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L,...., GrL,...,GmL (1 ≤r ≤m); and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R,...., GrR,...., GmR (1 ≤r ≤m), wherein:

the pixels connected to the source signal lines S1, S2,..., Sn are supplied with the selection signals from the first gate signal lines G1L, G2L,...., GrL,...,GmL;

the pixels connected to the source signal lines Sn+1, SN+2,..., S2n are supplied with the selection signals from the second gate signal lines G1R, G2R,...., GrL,...,GmR;

the selection signal starts to be supplied to the second gate signal line ~~G1R~~ GrR while the selection signal is supplied to the first gate signal line ~~G1L~~ GrL; and

the selection signal starts to be supplied to the first gate signal line ~~G2L~~ Gr+1L while the selection signal is supplied to the second gate signal line ~~G1R~~ GrR,

wherein the ~~m~~ r first gate signal lines G1L, G2L, ..., GrL,...., GmL of the first gate driver are not connected to the ~~m~~ r second gate signal lines G1R, G2R, ..., GrR,...., GmR of the second gate driver, ~~and~~

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate

wherein the selection signal to the first gate signal line Gr+1L rises to a static state after the selection signal to the first signal line GrL falls, and

wherein the selection signal to the first gate signal line Gr+1R rises to the static state after the selection signal to the first signal line GrR falls.

26. (Canceled)

27. (Canceled)